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| *TEST CASE DESCRIPTION* | | | | | | | |
|  | | Code | Version | | | Title | |
| Test Case | | 1.4.14 | 1 | | | Level transition from level NTC ASFA to level 1. Signal at the transition border in OS aspect (OS in further location) | |
|
| Baseline applicable | | Baseline 3 | | | | | |
| Test case author | | ADIF | | | | | |
| Test Objective(s) | | Verify that the transition from level NTC ASFA to level 1 OS is performed correctly when the on-board receives the level transition order. | | | | | |
| Diagram | |  | | | | | |
| Starting conditions | | Level | | | NTC ASFA | | |
| Mode | | | SN | | |
| Train Speed (km/h) | | | NR | | |
| Additional starting conditions | | | The signal located at the transition point is in OS aspect in the stabling track circuit. | | |
| Sequence of the Test Case | | Checkpoints | | | | | |
| Step | Step description | Interfaces | | Description of what to be tested at the interface | | | OK? |
| 1 | The train receives the level transition announcement via balise group. | DMI (O) | | Level 1 transition announcement | | |  |
| DMI (I) | |  | | |  |
| JRU | | M\_LEVEL = 1  M\_MODE = 13  Packet 41  D\_LEVELTR = D1  M\_LEVELTR = 2  L\_ACKLEVELTR = L1  DMI\_SYMB\_STATUS  LE10 | | |  |
| 2 | The EVC runs the distance “D1-L1” at which the acknowledgement window of the transition to Level 1 is shown to the driver. | DMI (O) | | Level 1 Acknowledgement is displayed | | |  |
| DMI (I) | |  | | |  |
| JRU | | Estimated front end = D1 – L1 – L\_DOUBTUNDER  DMI\_SYMB\_STATUS  LE11 | | |  |
| 3 | The driver acknowledges the level transition. | DMI (O) | | Level 1 Acknowledgement disappears | | |  |
| DMI (I) | | Driver acknowledges the level transition | | |  |
| JRU | | M\_DRIVERACTIONS = 7 | | |  |
| 4 | The train reads the main BG signal that sends OS mode profile at a further location and the level transition order. | DMI (O) | |  | | |  |
| DMI (I) | |  | | |  |
| JRU | | Packet 41  D\_LEVELTR = 32767  M\_LEVELTR = 2  Packet 12  Packet 80  D\_MAMODE=D2  M\_MAMODE=0  V\_MAMODE=Vos  L\_ACKMAMODE=L2  Q\_MAMODE=1  Packet 21  Packet 27 | | |  |
| 5 | Transition to L1 is performed. | DMI (O) | | Level 1 Symbol  FS Symbol  Level 1 transition announcement disappears | | |  |
| DMI (I) | |  | | |  |
| JRU | | M\_LEVEL= 2  M\_MODE = 0  DMI\_SYMB\_STATUS  LE03, MO11 | | |  |
|  |  |  | |  | | |  |
|  | |  | | |  |
|  | |  | | |  |
| 7 | The train is approaching to OS area and the EVC supervises the entry in OS area as an EoA without release speed. | DMI (O) | | Braking curve to the entry point of OS area  Vtarget=0  Vpermitted and Dtarget decrease | | |  |
| DMI (I) | |  | | |  |
| JRU | | SPEED AND DISTANCE MONITORING INFORMATION  V\_PERM decreases  V\_TARGET=0  M\_SDMTYPE=1 | | |  |
| 8 | The train is inside the acknowledgement window running at a speed lower than the permitted speed in OS mode. | DMI (O) | | Vtrain<Vos  Acknowledgment of OS mode message | | |  |
| DMI (I) | |  | | |  |
| JRU | | V\_TRAIN<V\_MAMODE  L2≥D2-D\_LRBG1  DMI\_SYMB\_STATUS  MO08 | | |  |
| 9 | The driver acknowledges the request for OS mode before reaching the beginning of the OS area. | DMI (O) | | Acknowledgment of OS mode message disappears | | |  |
| DMI (I) | | Driver acknowledges OS mode | | |  |
| JRU | | M\_DRIVERACTIONS =0 | | |  |
| 10 | The EVC switches to OS mode before reaching the beginning of the stabling track circuit. | DMI (O) | | OS mode symbol | | |  |
| DMI (I) | |  | | |  |
| JRU | | M\_LEVEL=2  M\_MODE=1  DMI\_SYMB\_STATUS  MO07 | | |  |
| Final state | | Level | | 1 | | |  |
| Mode | | OS | | |  |
| Train Speed (km/h) | | NR | | |  |
| Other parameters | |  | | |  |
| Final Test Result | |  | | | | | |
| Field of Application | | Spain | | | | | |
| Briefing instructions | |  | | | | | |