|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| *TEST CASE DESCRIPTION* | | | | | | | | | |
|  | | | Code | Version | | | Title | | |
| Test Case | | | 1.5.1 | 1 | | | Mode transition from FS to OS at a further location. The driver acknowledges the request before reaching the OS area. | | |
|
| Baseline applicable | | | Baseline 2 | | | | | | |
| Test case author | | | ADIF | | | | | | |
| Test Objective(s) | | | Verify that the EVC switches from FS to OS at a further location. | | | | | | |
| Diagram | | |  | | | | | | |
| Starting conditions | | | Level | | | | | 1 | |
| Mode | | | | | FS | |
| Train Speed (km/h) | | | | | NR | |
| Additional starting conditions | | | | | Train located in rear of the BG associated to a main signal that shows OS aspect. | |
| Sequence of the Test Case | | | Checkpoints | | | | | | |
| Step | Step description | | Interfaces | | | Description of what to be tested at the interface | | | OK? |
| 1 | The train reads the main signal BG that sends OS mode profile at a further location. | | DMI (O) | | | FS mode symbol | | |  |
| DMI (I) | | |  | | |  |
| JRU | | | M\_MODE=0  (LRBG1)  Packet 12  Packet 80  D\_MAMODE=D1  M\_MAMODE=0  V\_MAMODE=Vos  L\_ACKMAMODE=L1 | | |  |
| 2 | The EVC supervises the beginning of the OS area as an EoA without release speed. | | DMI (O) | | | Braking curve to the beginning of the OS area  Vtarget=0  Vpermitted and Dtarget decrease | | |  |
| DMI (I) | | |  | | |  |
| JRU | | | V\_PERMITTED decreases  V\_TARGET=0 | | |  |
| 3 | The train is inside the acknowledgement window running at a speed lower than the permitted speed in OS mode. | DMI (O) | | | Vtrain<Vos  Acknowledgment of OS mode message | | | |  |
| DMI (I) | | |  | | | |  |
| JRU | | | V\_TRAIN<V\_MAMODE  L1≥D1-D\_LRBG1  START DISPLAYING TEXT MESSAGE | | | |  |
| 4 | The driver acknowledges the request for OS mode before reaching the beginning of the OS area. | DMI (O) | | | Acknowledgment of OS mode message disappears | | | |  |
| DMI (I) | | | Driver acknowledges OS mode | | | |  |
| JRU | | | M\_DRIVERACTIONS =0  STOP DISPLAYING TEXT MESSAGE | | | |  |
| 5 | The EVC switches to OS mode before reaching the beginning of the stabling track circuit. | DMI (O) | | | OS mode symbol | | | |  |
| DMI (I) | | |  | | | |  |
| JRU | | | M\_MODE=1 | | | |  |
| Final state | | | Level | | | 1 | | |  |
| Mode | | | OS | | |  |
| Train Speed (km/h) | | | NR | | |  |
| Other parameters | | |  | | |  |
| Final Test Result | | |  | | | | | | |
| Field of Application | | | Spain | | | | | | |
| Briefing instructions | | |  | | | | | | |